


Sri Sai Ram Engineering College
Department of Electrical and Electronics Engineering

1.NAME: P.RAJAKUMAR	
2.DESIGNATION	Assistant Prof (Grade-II)
3.QUALIFICATION	M.E. (Ph.D)
4.AREA OF SPECIALIZATION	POWER ELECTRONICS & DRIVES
5.EXPERIENCE:	Teaching: UG: 7 YRS 8 Months PG: 1 YR Industry : 5 YRS and 8 Months
6.NO.OF WORKSHOP / SDP / FDP attended:	3/1/3
7.PUBLICATIONS :	JOURNAL National : - International : 1 CONFERENCE National : 2 International : -
8. RESEARCH GUIDANCE:	NIL
9. GENERAL	NIL
10. STAFF ACHIEVEMENTS:	* 3 TIMES PRODUCED 100% RESULTS FOR PG STUDENTS(PED) * 1 TIME CASH AWARD RECEIVED FOR 100% RESULTS at SEC. *CASH AWARD RECEIVED 3 TIMES FOR BEST RESULTS at Vel S.R.S College of Multimedia.

EXPERIENCE:

1. Assistant Professor(Grade-II) in Sri Sairam Engineering College From 21.01.2011 to till date
2. Senior Lecturer in GKM College of Engineering & Technology From 29.06.2009 to 19.01.2011
3. Senior Lecturer in KCG College of Technology From 1.08.2008 to 23.06.2009
4. Lecturer in KCG College of Technology From 23.08.2007 to 31.07.2008
5. Lecturer in Vel Sri Rangarajan Sakunthala College of Multimedia[Engg] From 26.05.2003 to 22.08.2007
6. Senior .Electrical Engineer in Media Electrical & Electronics Services Pvt Ltd, Republic of Maldives. From 4.12.2001 to 26.04.2003
7. Design Engineer in Ignition Products (India) Pvt Ltd From 5.12.1996 to 04.04.2001

100 % Result produced for the following subjects:

S.No	Sub Code	Sub Name	Class/ Sem /Sec	Year
1.	235208	MICROCONTROLLER AND DSP BASED SYSTEM DESIGN	M.E/II (PED)	2011-2012
2.	235081	MODERN RECTIFIER AND RESONANT CONVERTER	M.E/III (PED)	2012-2013
3.	PE9252	ADVANCED POWER SEMICONDUCTOR DEVICES	M.E/I (PED)	2012-2013

Workshop/Conferences/FDP:

- AICTE sponsored SDP on “Computational Intelligence on Power System Analysis,Control and Optimization,” at Pondicherry Engineering College.
- Workshop on “Electro Magnetic Theory Revisited”, at MIT, AU, Chennai.
- Workshop on “Digital System Design using VHDL”, at Vel Hi-Tech College of Engineering.

- Faculty development Programme on “Modern method of Teaching”. at Vel Hi-Tech College of Engineering.
- Faculty Development Programme on “Power Electronics”, at Easwari Engineering College.
- Faculty development Programme on “FACTS”. at SSN College of Engineering.
- Quality Improvement Programme on “Instructional design and delivery”, at Vel MultiTech College of Engineering.

Publications:

- Published a Paper title “Review on Power Quality Issues” at IRACST-Engineering Science and Technology: an International Journal, Vol2, no1, pp:90-95, Feb’2012.
- Presented a paper titled “ Power Quality Improvement Using STATCOM based Cascaded Multilevel Inverter” in National level (ETIPS’08) conference held in Vel Multitech SRS Engineering College, Avadi on 8.08.2008.
- Presented a paper titled “Power Quality Improvement using STATCOM based Cascaded Multilevel Inverter” in National level conference (ECLECTIC ‘08) held in Vivekananda College of Engineering for woman, Thiruchengode on 14.03.08.

Written three monographs for the subjects (Inside the Campus),

1. Power Electronics,
2. Power System Analysis
3. Network Analysis and Synthesis.