


Sri Sai Ram Engineering College
Department of Electrical and Electronics Engineering

Name & Photo : C.Nayanatara	
Designation:	Assistant Prof G-II
Qualification :	B.E, MBA, M.Tech,(Ph.D)
Area of Specialisation :	MBA(Marketing) M.Tech(Power Systems)
Experience :	Teaching : UG: 6yrs-10 months PG :
	Industry : 2 yrs
No. of workshop / Conferences / FDP attended :	Workshop-3 Conferences-3
Publication :	Journal National : International :
	Conference National : 2 International : 1
Research Guidance :	
General :	<ol style="list-style-type: none"> 1. In charge of Electrical Devices & Circuits lab. 2. Class Coordinator. 3. Acted Timetable Coordinator. 4. Acted Conference Convener for the year-2010
Staff Achievements: -	<p>Received Cent Percent achiever credit for the year 2007 - 2008 and 2008 – 2009.</p> <p>Obtained BEST PAPER AWARD for the paper titled 'Available Transfer Capacity assessment and enhancement using</p>

	FACTSdevices' in the National Conference on Research Scholars on Application of Emerging Technologies at ADHIYAMAAN COLLEGE OF ENGINEERING, Hosur
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Experience: 8yrs-10 months

Total Teaching Experience -6 years, 10 months

- Worked as Senior Academic Counselor in **Innovative CAD GrafX Private Limited** from July 2001 to July 2003.
- Worked as Lecturer in Valliammai Engineering College from Aug 2003 to May 2007.
- Working as Senior Lecturer in **Sri Sai Ram Engineering College** since June 2007 to till date.

Workshop/Conferences/FDP:

- ❖ Workshop on Embedded System and its applications at SRM University.
- ❖ Workshop on **'PLC and its Applications'** organized by the Department of Electrical and Electronics Engineering, Sri Sai Ram Engineering College.
- ❖ Workshop on **'An Overview of OrCAD'** organized by the Department of Electrical and Electronics Engineering, Sri Sai Ram Engineering College.
- ❖ Short Term Training Programme on **'Modeling of FACTS Devices and its Applications'** conducted by the Department of Electrical and Electronics Engineering, SRM University.
- ❖ Faculty Development Programme on **'Electro magnetic Theory'** conducted by the Department of Electrical and Electronics Engineering, Valliammai Engineering College.
- ❖ Faculty Development Programme on **'Control Systems'** conducted by the Department of Electrical and Electronics Engineering, Sri Sai Ram Engineering College.
- ❖ Faculty Development Programme on **'VHDL Programming'** conducted by the Department of Electrical and Electronics Engineering, Sri Sai Ram Engineering College.
- ❖ Presented a Paper titled **'Available Transfer Capacity assessment and enhancement using FACTSdevices'** in the National Conference on Research Scholars on Application of Emerging Technologies at ADHIYAMAAN COLLEGE OF ENGINEERING, Hosur

- ❖ Presented a Paper titled '**Congestion Management by determining optimal location of TCSC using Genetic Algorithm**' in the National Conference on Computational Intelligence in Power Apparatus and System at SRM UNIVERSITY.
- ❖ Presented a Paper titled '**Total Social welfare Maximization**' in the International Conference on Electrical Engineering at PONDICHERYENGINEERING COLLEGE.